Module 3: Product Description

3.2.2.2 Design Parameters and Test Data

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One of the essential parts of the PMP S.A.'s development process is the design verification step, where the product design is evaluated, if it meets all identified needs and intended use as documented in the Design Inputs. The summary of the findings are recorded in the Design Verification Report (DVR).

1. DESIGN VERIFICATION OF IQOS 3

(b) (4)

Information about the devices that were evaluated as part of the design verification process are summarized in the Table 1 (b) (4)

(b) (4)

Table 1: PDIMS codes of Device Under Tests (DUT)[1]

DUT	Color	Product Codes
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(b) (4)

(b) (4) The overview of these DV builds in provided in the table below.

Table 2: Device Under Test (DUT) Builds and PDIMS Code & Revision

			PDIMS Code & Revision	
DV Build	**Top Level Build Start Date**	**Top Level Build End Date**	**Holder**	**Charger**
(b) (4)				

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Table 3: Summary of the critical design verification results for *IQOS* 3 System

Design Input ID	Design Input Description	Criticality	Test Specification ID	Related Design Verification results	DECISION (PASS/FAIL)	Comment
(b) (4)						

(table continues)

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(table continues)

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(b) (4)						

The overview of the deviations, defects and non-conformances (NC) is provided in the Table 4.

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Table 4: List of deviations, defects and non-conformances (NC)

Dev. #	Type	Related Design Input ID	Description of Deviation	Link to the Non-Conformance Form or Design Verification Results (HP ALM ID)	Criticality	Final Decision	Justification
(b) (4)							

(table continues)

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(b) (4)	Most importantly, all the required safety and regulatory requirements are met.

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